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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

ROBERT L. PAYNE, et al.

US 018186

Serial No. 09/997,609

Filed: Nov. 29, 2001

Group Art Unit: 2661

Title: High-Speed Interchip Interface Protocol

Commissioner for Patents  
Washington, D.C. 20231

LETTER TO OFFICIAL DRAFTSMAN

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Sir:

Enclosed are formal drawings for filing in the  
above-identified application.

Respectfully submitted,

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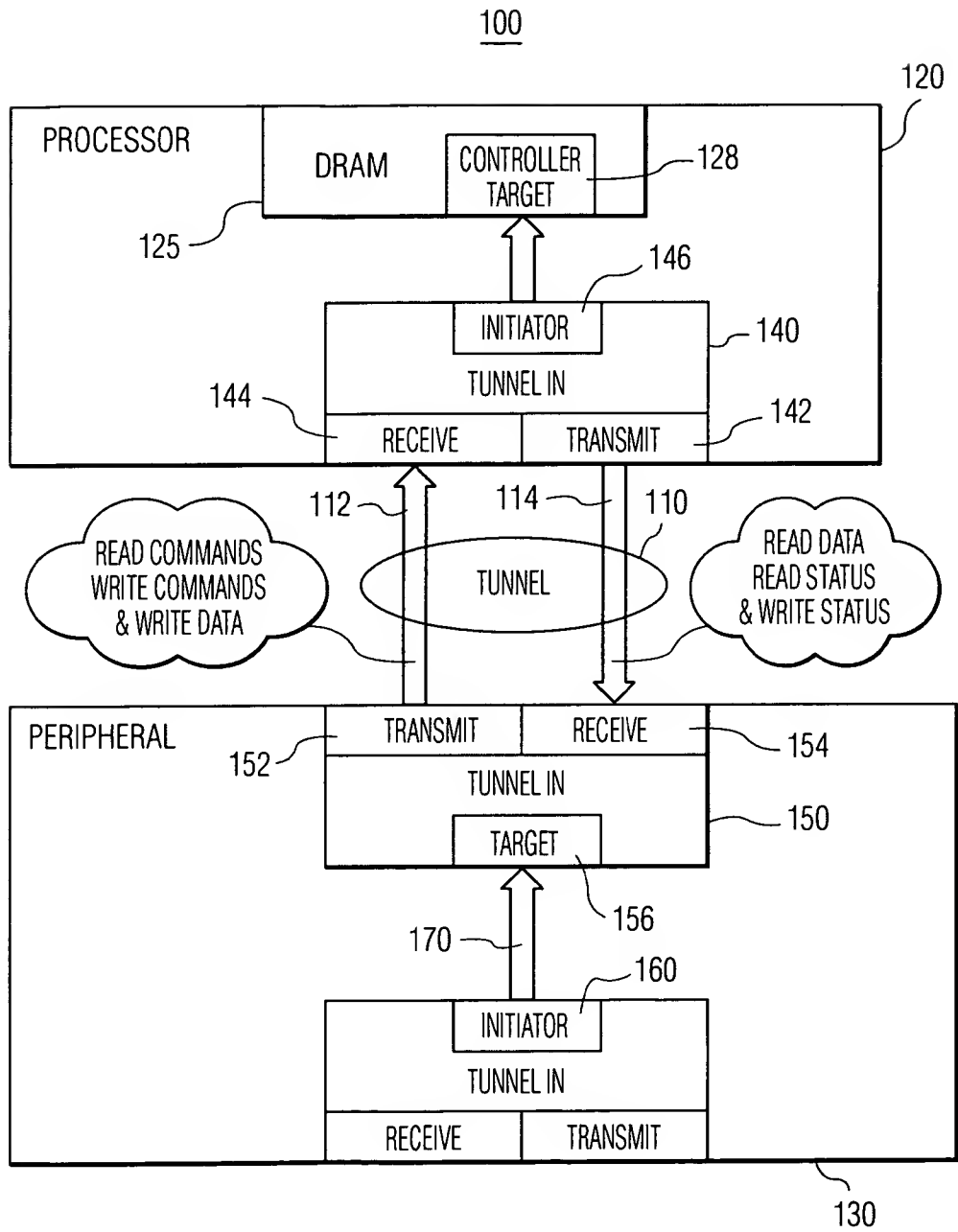


FIG. 1

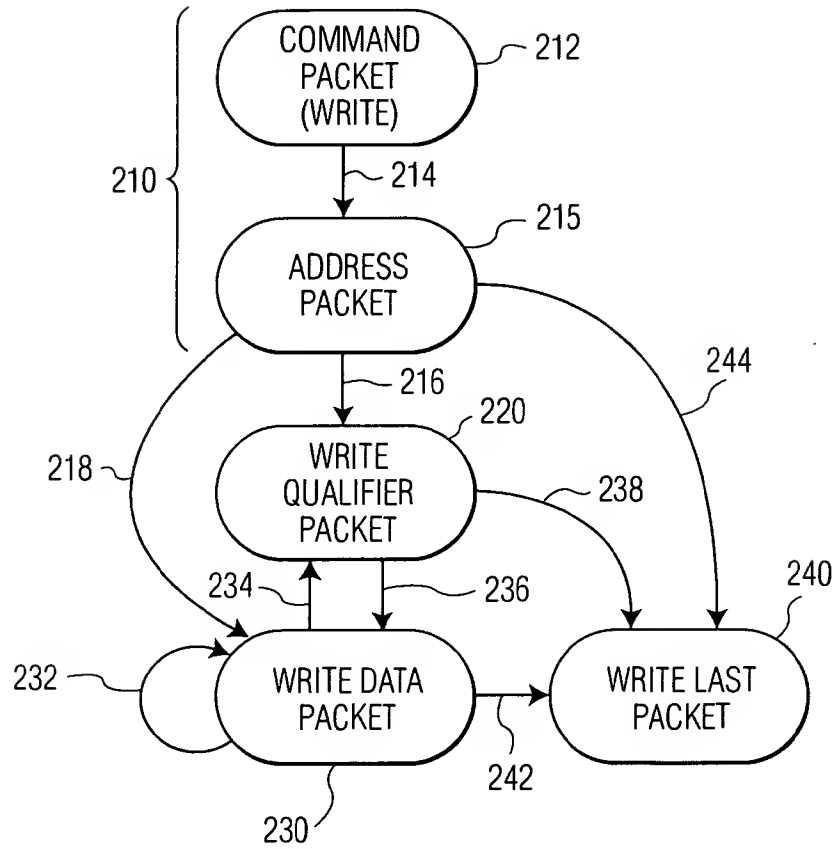


FIG. 2A

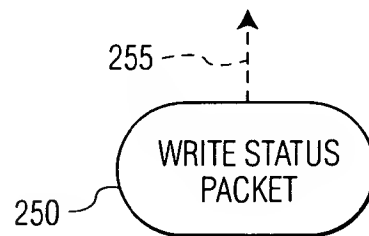


FIG. 2B

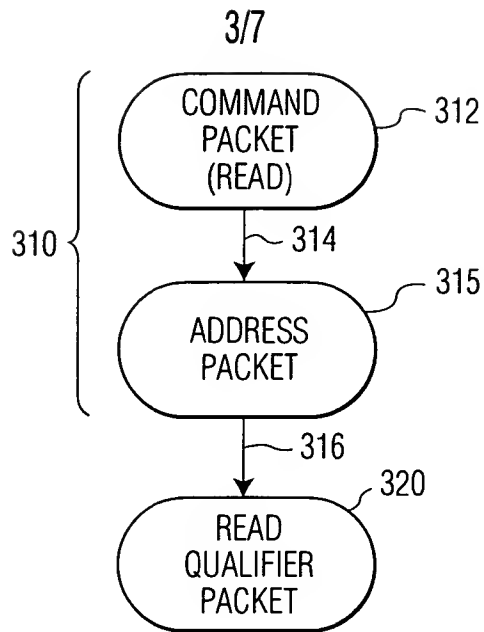


FIG. 3A

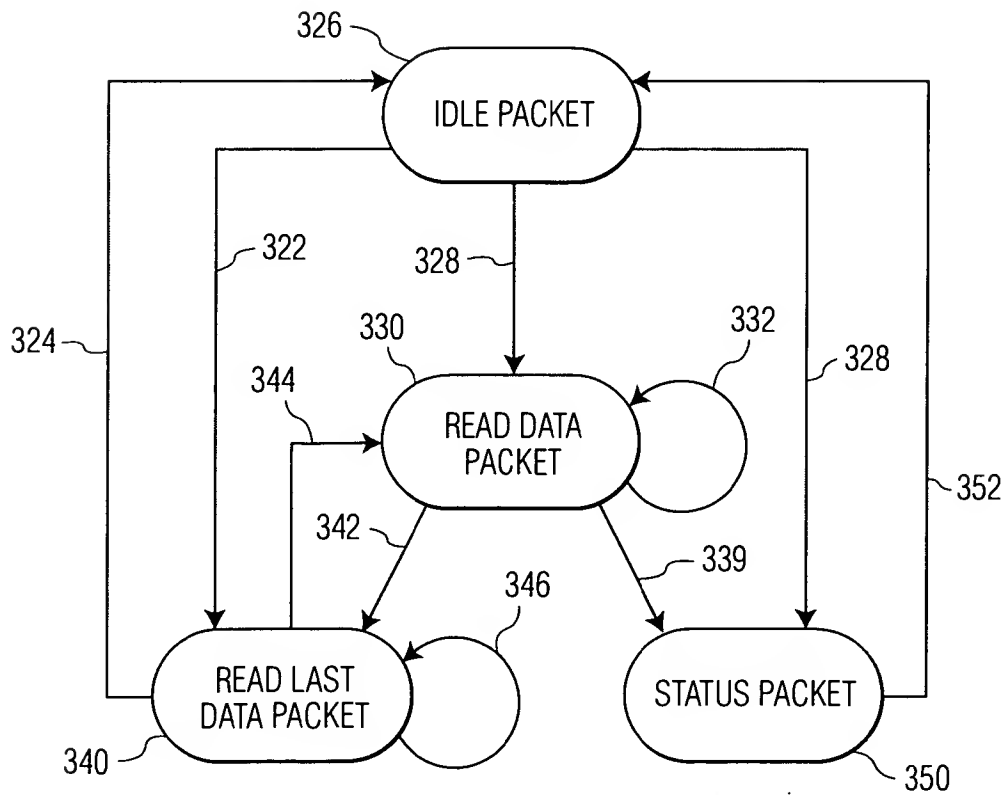


FIG. 3B

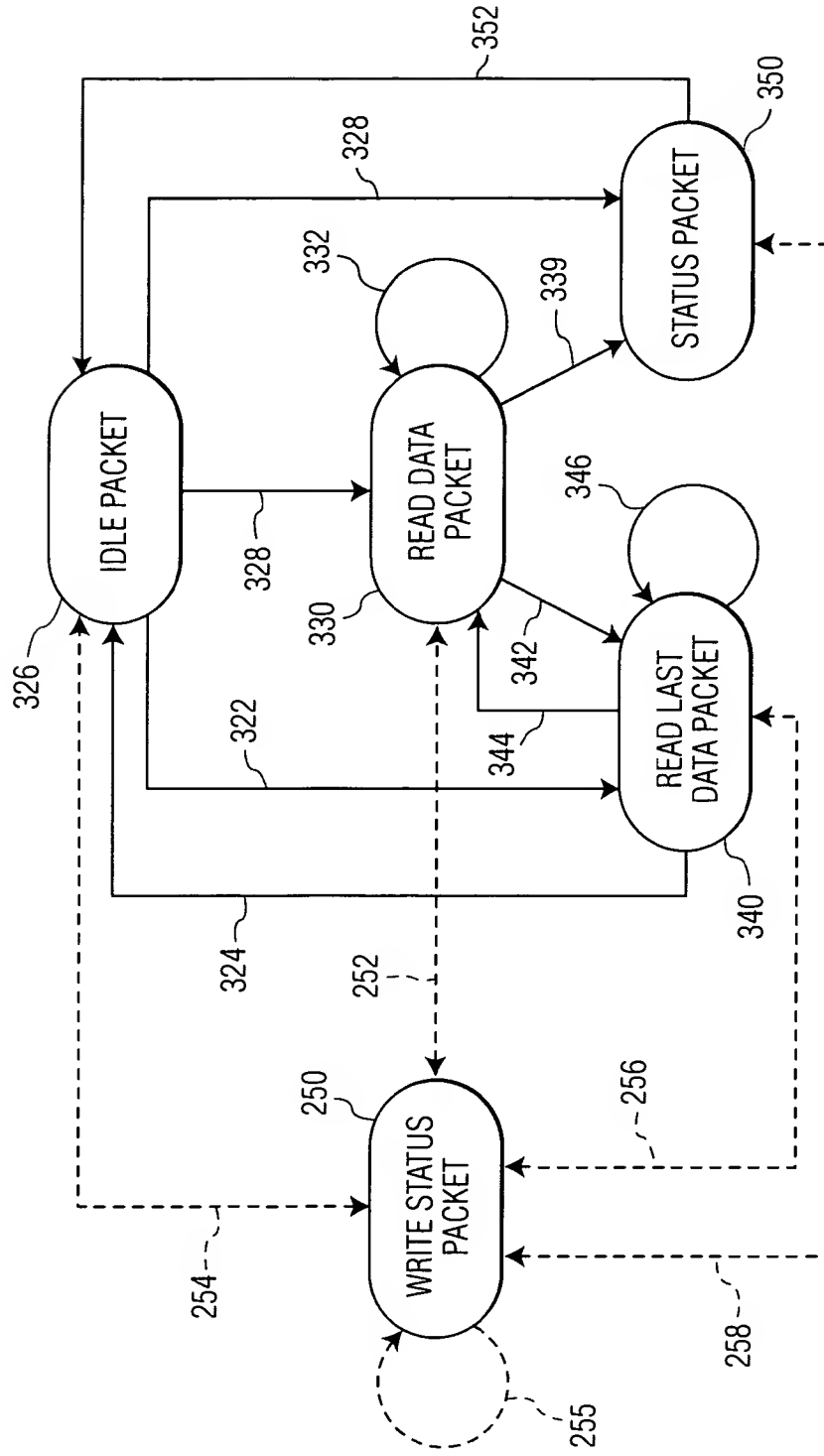


FIG. 4

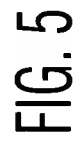


FIG. 5

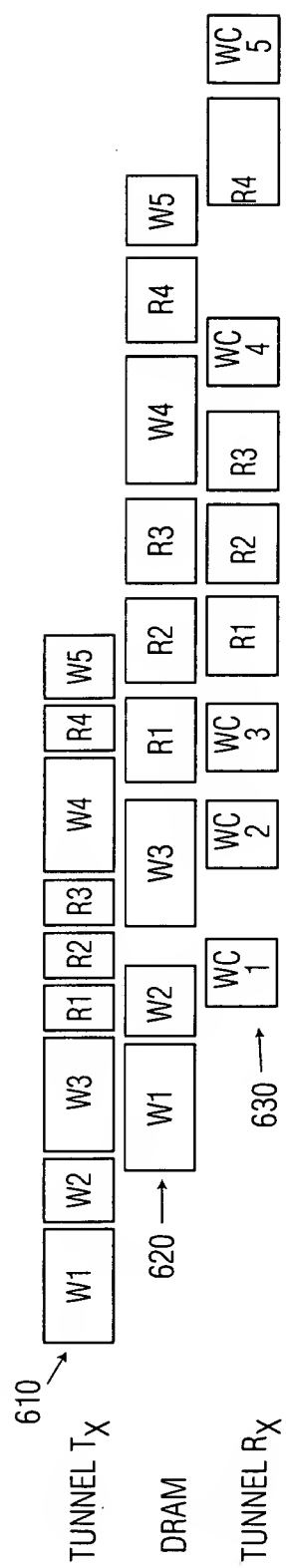
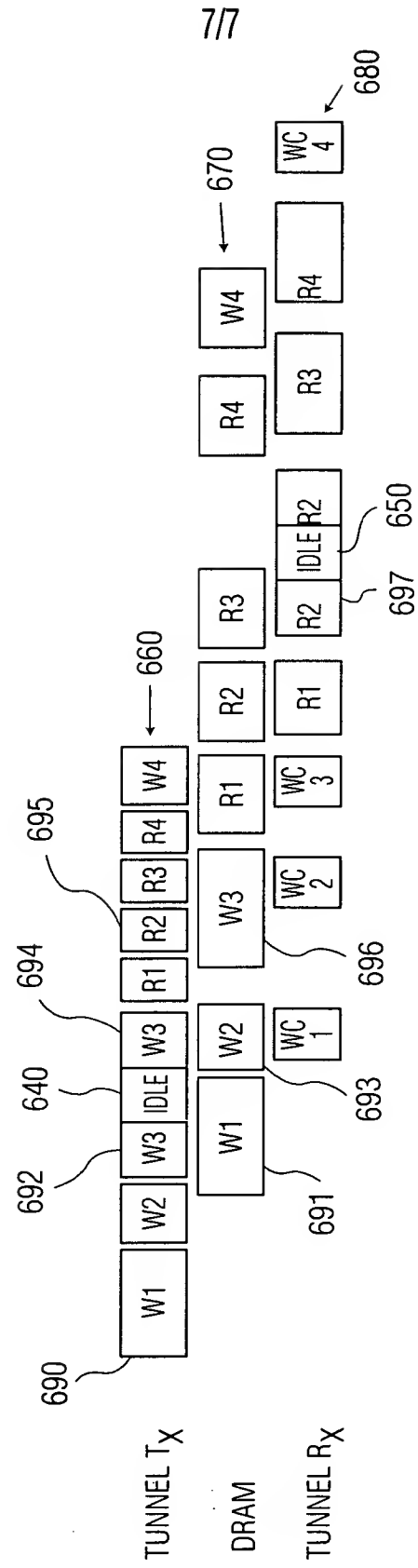


FIG. 6A



**FIG. 6B**